

**AMENDMENTS TO THE SPECIFICATION:**

**Please amend the paragraph bridging pages 6 and 7, beginning at page 6, line 26, as follows:**

Comparing FIG. 4 with FIG. 3, it is apparent that the variable drive current driver circuit according to the second embodiment differs from the variable drive current driver circuit according to the first embodiment in that a control circuit, including transistors PMOS 24, PMOS 25, PMOS 26, NMOS 25, NMOS 26 and NMOS 27, is added to an output stage. In the variable drive current driver circuit according to the first embodiment, the control circuit is added not to the output stage but to the constant current source side. In FIG. 4, the voltage  $V_a$  is constant. In a case where the logic value of the control signal is "1," currents  $I_{c1}$  and  $I_{c2}$  flow. In a case where the logic value of the control signal is "0," currents  $I_{c1}$  and  $I_{c2}$  do not flow. When the logic value of the control signal is "1," therefore, the sum of currents  $I_{c1}$  and  $I_{d1}$  or the sum of currents  $I_{c2}$  and  $I_{d2}$  becomes the drive current. When the logic value of the control signal is "0," only the current  $I_{d1}$  or  $I_{d2}$  becomes the drive current. Accordingly it is possible that  $I_{c1} = I_{c2}$  and  $I_{d1} = I_{d2}$ .